

**AMENDMENTS TO THE DRAWINGS:**

The attached drawing(s) include changes to FIGS. 1, 2, and 3, to designate same by the legend - -PRIOR ART- -.

Approval of these changes and entry of the corrected Drawings is respectfully requested.

**REMARKS**

In accordance with the foregoing, corrected FIGS. 1-3 are supplied, responsive to Item 1 of the Action, the original claims are cancelled and new claims 36-50 are presented. It should be noted that these new claims are not directed to new subject matter but rather have a basis in the original claims to which the current Action is directed. For example, new independent claim 36 combines recitations of (cancelled) original claims 34, 21, and 14, new claims 37-42 correspond to original claims 6-9, 11 and 19, new claim 43/36 relates to original claim 8/1 and is based on the configuration of FIGS. 14 and 24 and new claims 44 through 50 correspond to original claims 26-31 and 35.

Claims 36-50 are pending and under consideration.

**ITEM 3: REJECTION OF CLAIMS 1-14 FOR ANTICIPATION UNDER 35 U.S.C. §102(b) BY U.S. PATENT 5,502,412 (CHOIE ET AL.); AND**

**ITEM 6: REJECTION OF CLAIMS 15-35 FOR OBVIOUSNESS UNDER 35 U.S.C. §103(a) OVER U.S. PATENT '412 (CHOIE ET AL.) IN VIEW OF U.S. PATENT APPLICATION PUBLICATION 2002/0175883 TO ONOZAWA ET AL.**

The rejections are respectfully traversed.

As described in new independent claim 36, the present invention relates to a plasma display apparatus comprising a pre-drive circuit having a plurality of drive systems which have the same structure and are provided within an IC formed on a common semiconductor chip.

Drive circuits of a plasma display apparatus must drive electrodes of the apparatus with driving signals at high voltages and at very high speeds. Therefore, precise timing of the drive signals is necessary and must be maintained in spite of variations of environmental conditions.

The drive circuits of the present invention can satisfy the above requirements, because the plurality of drive systems of the pre-drive circuits have a common structure and are provided within an IC formed on a common semiconductor chip. Choi et al. (U.S. Patent 5,502,412 - - "Choi" - -) has no description of a plasma display apparatus, nor does it disclose a constitution in which a plurality of drive systems of a pre-drive circuit have a common constitution. Further, Choi does not show a constitution in which the plurality of drive systems are provided within an IC formed on a common semiconductor chip.

The Examiner states that Choi shows a constitution in which each drive system has the same constitution, in FIG. 3. This is incorrect. Elements designated by references 76B-76E

constitute a high level shift circuit and these elements are not provided for a circuit designated by reference 72. In other words, the constitution of the circuit designated by reference 76 is different from that of the circuit designated by reference 72.

Onozawa et al. (U.S. Patent Application Publication No. 2002/0175883 - - "Onozawa" does not describe that each pre-drive circuit has the same constitution and that the pre-drive circuits are provided within an IC formed on the same semiconductor chip.

#### **PERTINENT REFERENCE WO 02095914 ('914)**

The subject '914 reference was cited in an Office Action for an application filed in the Republic of China (Taiwan), corresponding to the subject application, and which '914 reference further was cited in an Information Disclosure Statement filed herein on September 16, 2005 (see, Form PTO-1449 Reference: BC and copy thereof supplied with the referenced Information Disclosure Statement).

WO 02095914 discloses in FIGS. 2-5 an arrangement in which a plurality of drive systems of a pre-drive circuit have a common constitution; however, it does not show a constitution in which a plurality of drive systems are provided within an IC formed on a common semiconductor chip. Please note that FIGS. 2-5 of WO 02095914 show two different gate drivers 8 and 8'. WO 02095914 has no description that the gate drivers 8 and 8' are provided within an IC formed on a common semiconductor chip.

Generally speaking, timing differences between prior art drive circuits had not been required to be as strict as is required for drive circuits for a plasma display apparatus. Therefore, the prior art constitution in which the gate drivers are provided in different ICs, as shown in WO 02095914, created no problems. However, when such drive circuits are adapted for use as drive circuits of a plasma display, particularly a plasma display apparatus having a power recovery circuit, the inventors found the occurrence of problems and thus made the present invention. Therefore, it is submitted that the invention as claimed herein is not obvious from WO 02095914.

#### **CONCLUSION**

In accordance with the foregoing, it is respectfully submitted that the pending claims patentably distinguish over the references of record and the additional citation of WO 02095914.

There being no further outstanding objections or rejections, it is submitted that the application is in condition for allowance. An early action to that effect is courteously solicited.

Finally, if there are any formal matters remaining after this response, the Examiner is requested to telephone the undersigned to attend to these matters.


If there are any additional fees associated with filing of this Amendment, please charge the same to our Deposit Account No. 19-3935.

Respectfully submitted,

STAAS & HALSEY LLP

Date: October 24, 2005

By: \_\_\_\_\_

  
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